

FIG. 1



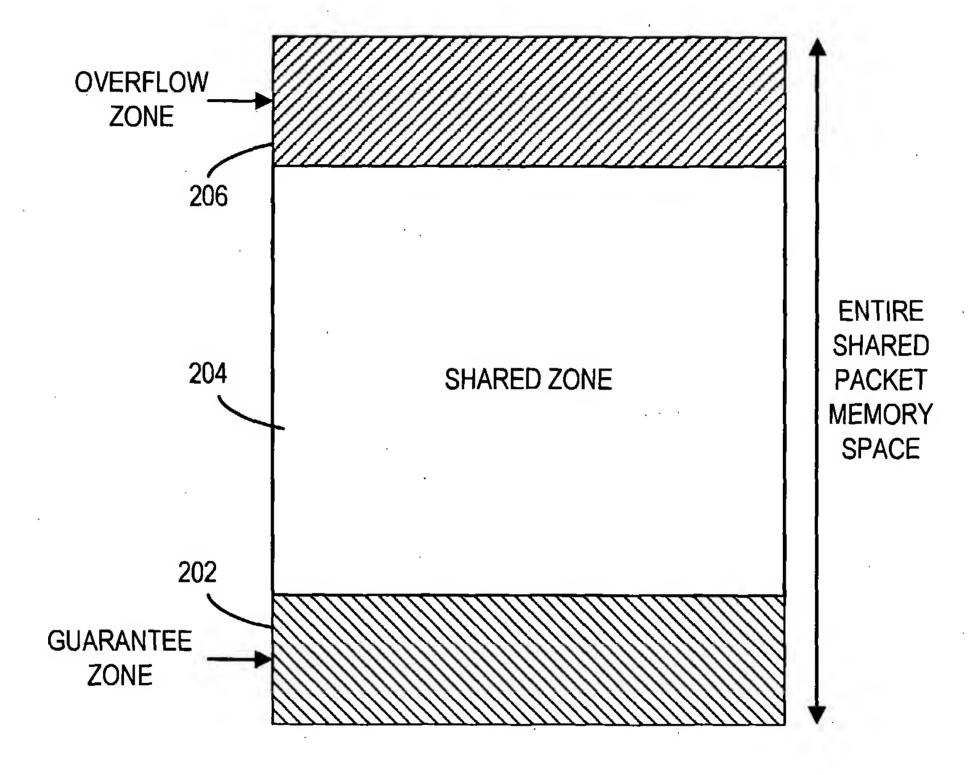
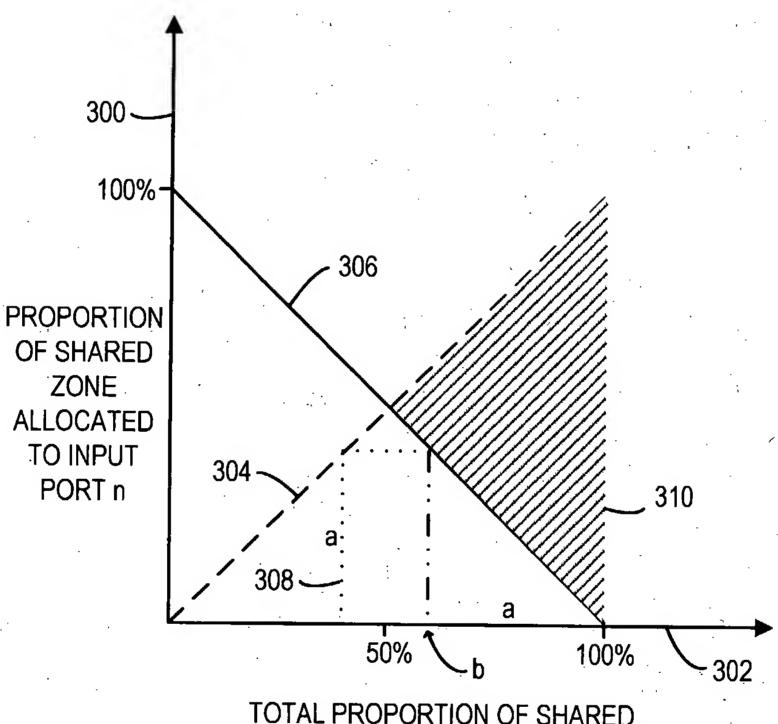
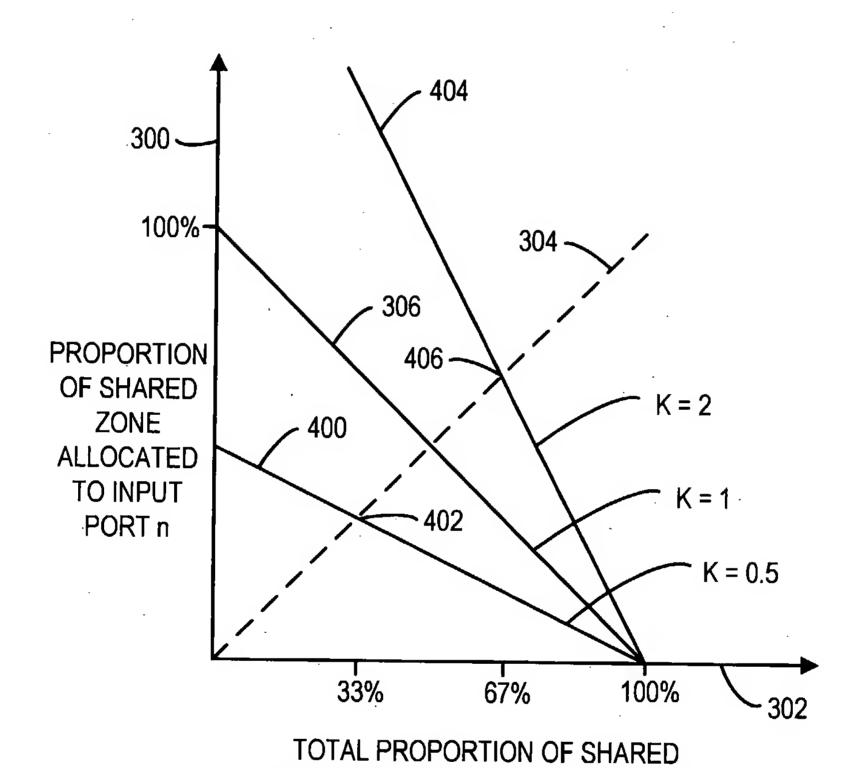


FIG. 2



TOTAL PROPORTION OF SHARED ZONE ALLOCATED TO INPUT PORTS



ZONE ALLOCATED TO INPUT PORTS

FIG. 4

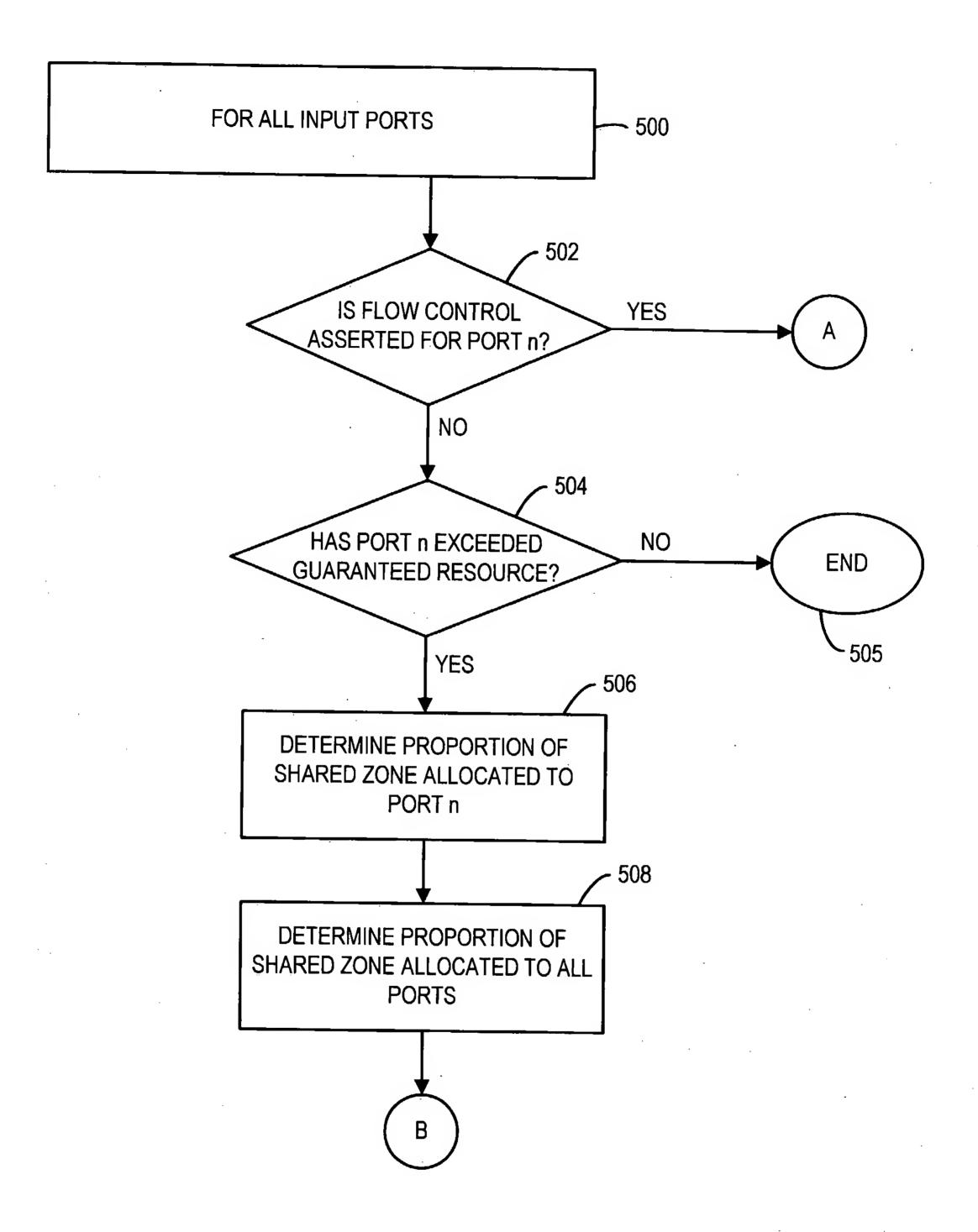


FIG. 5A

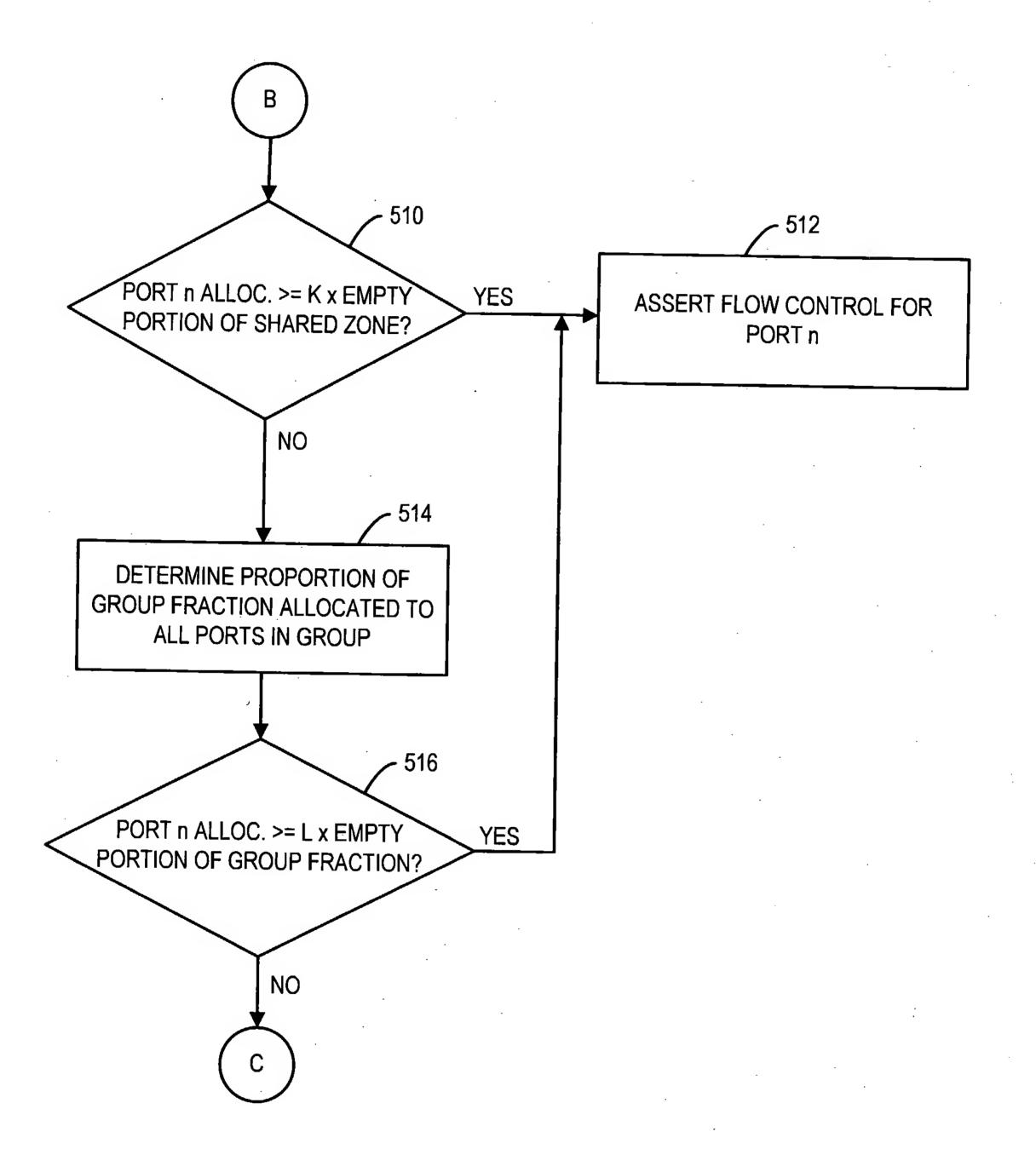


FIG. 5B

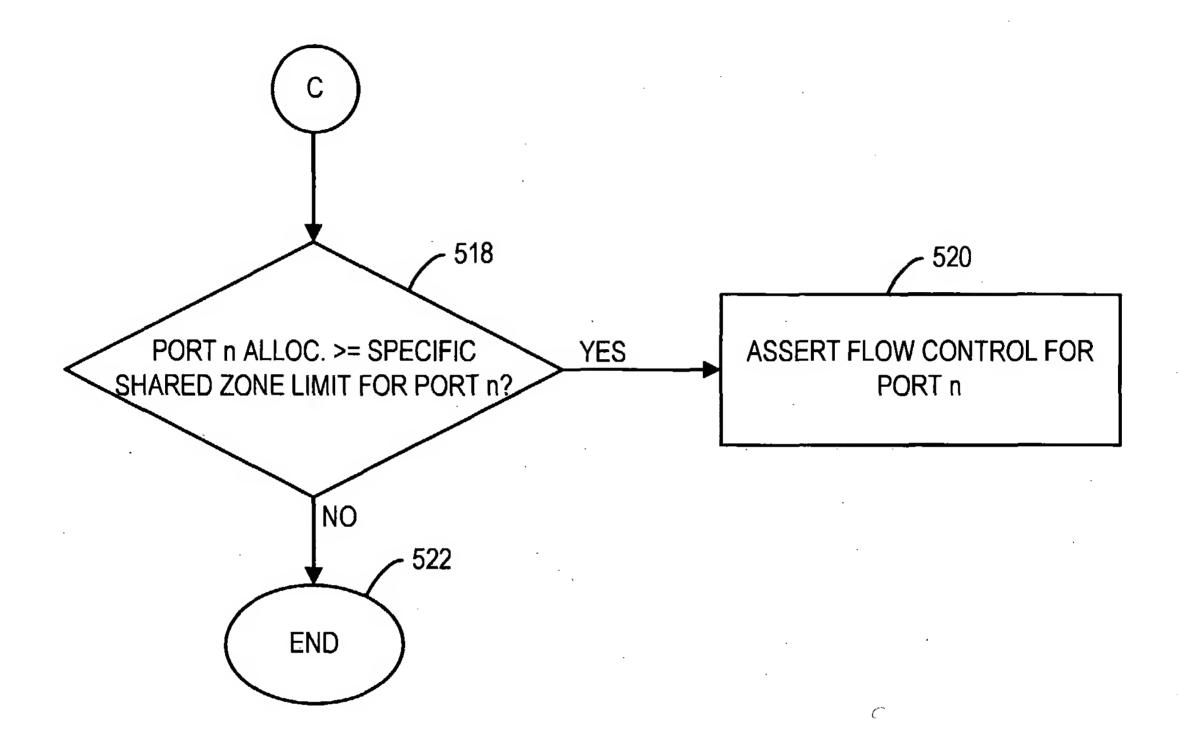
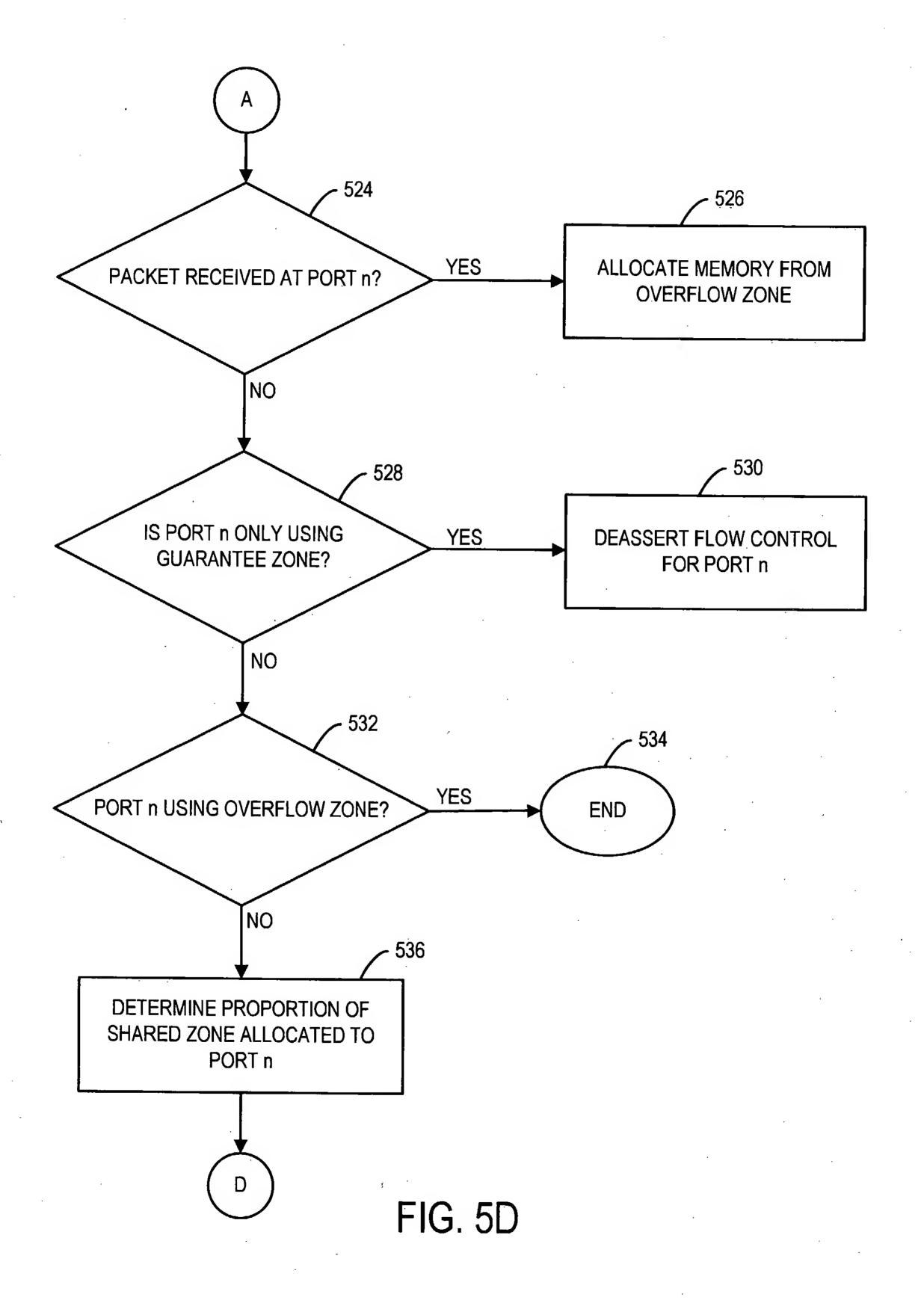


FIG. 5C



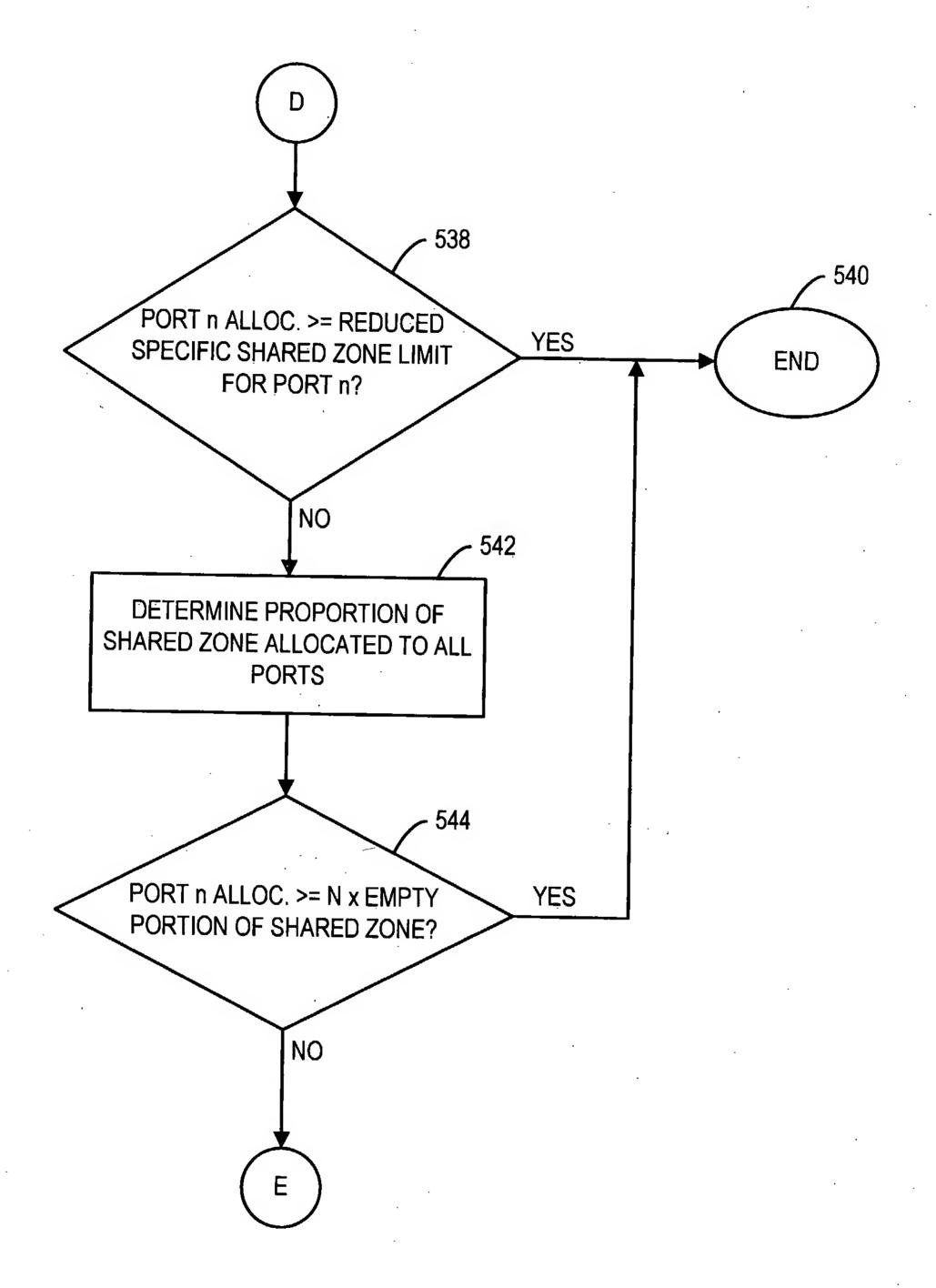


FIG. 5E

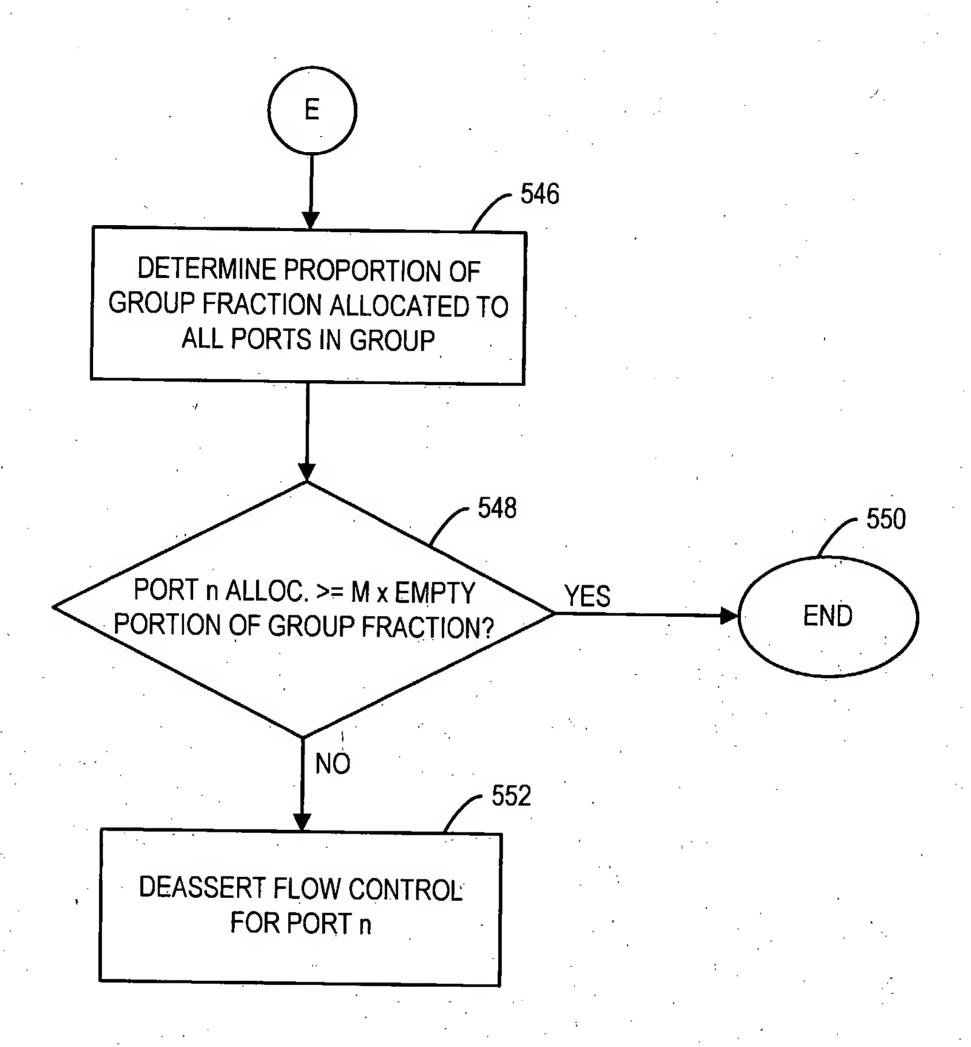


FIG. 5F

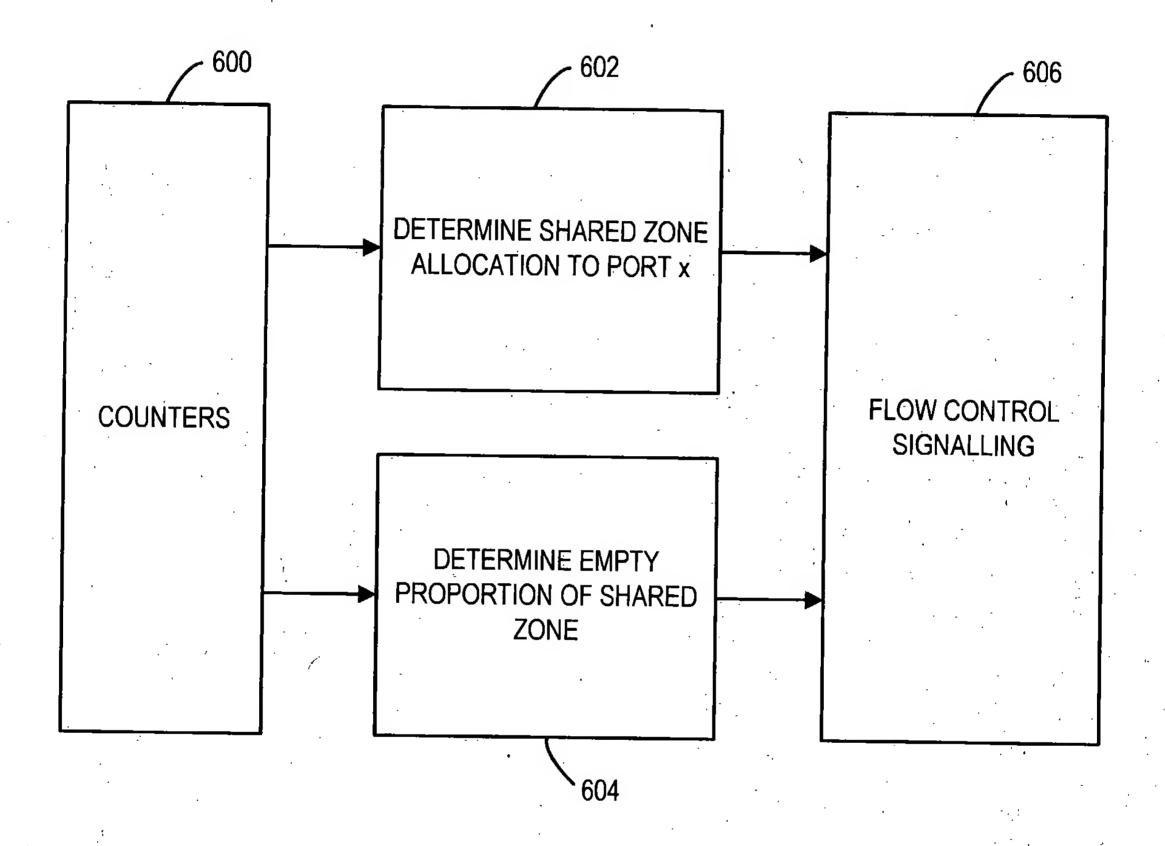


FIG. 6